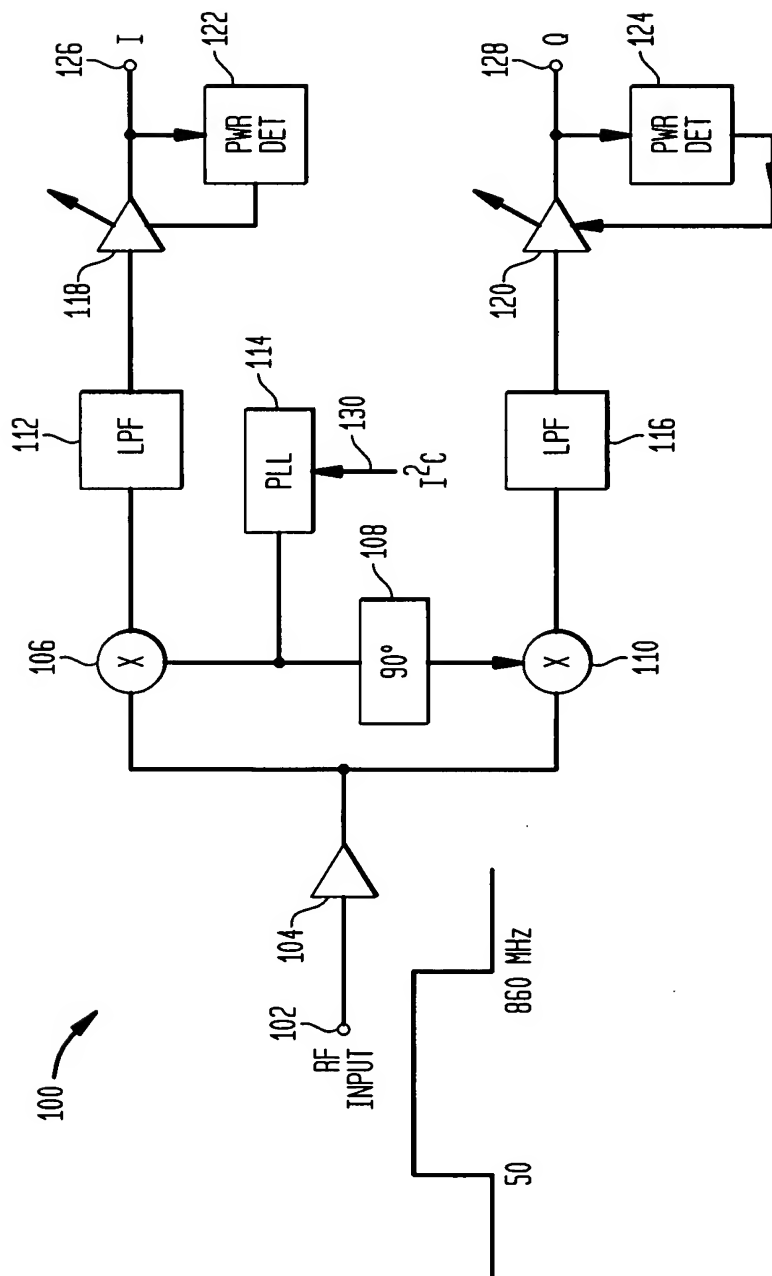




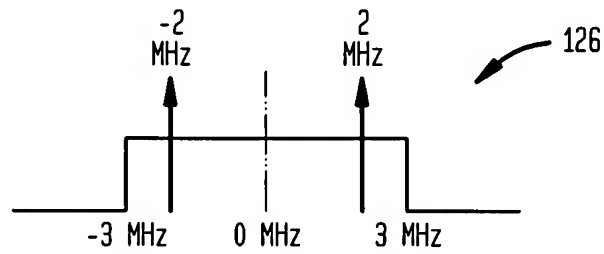
FIG. 1



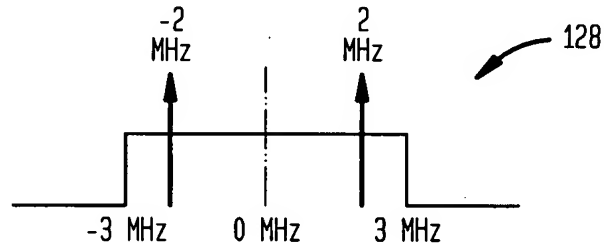
**FIG. 2A**  
RF INPUT



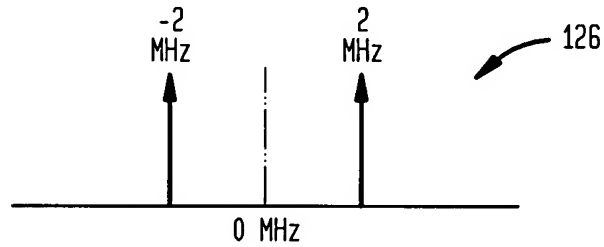
**FIG. 2B**  
I OUTPUT



**FIG. 2C**  
Q OUTPUT



**FIG. 2D**  
I OUTPUT  
(BLANKING)



**FIG. 2E**  
Q OUTPUT  
(BLANKING)

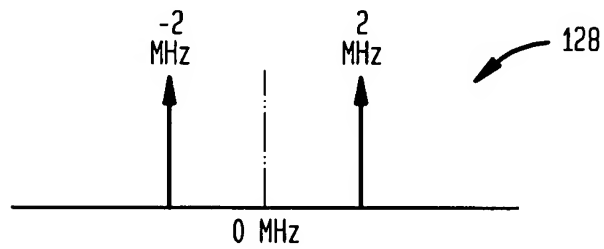
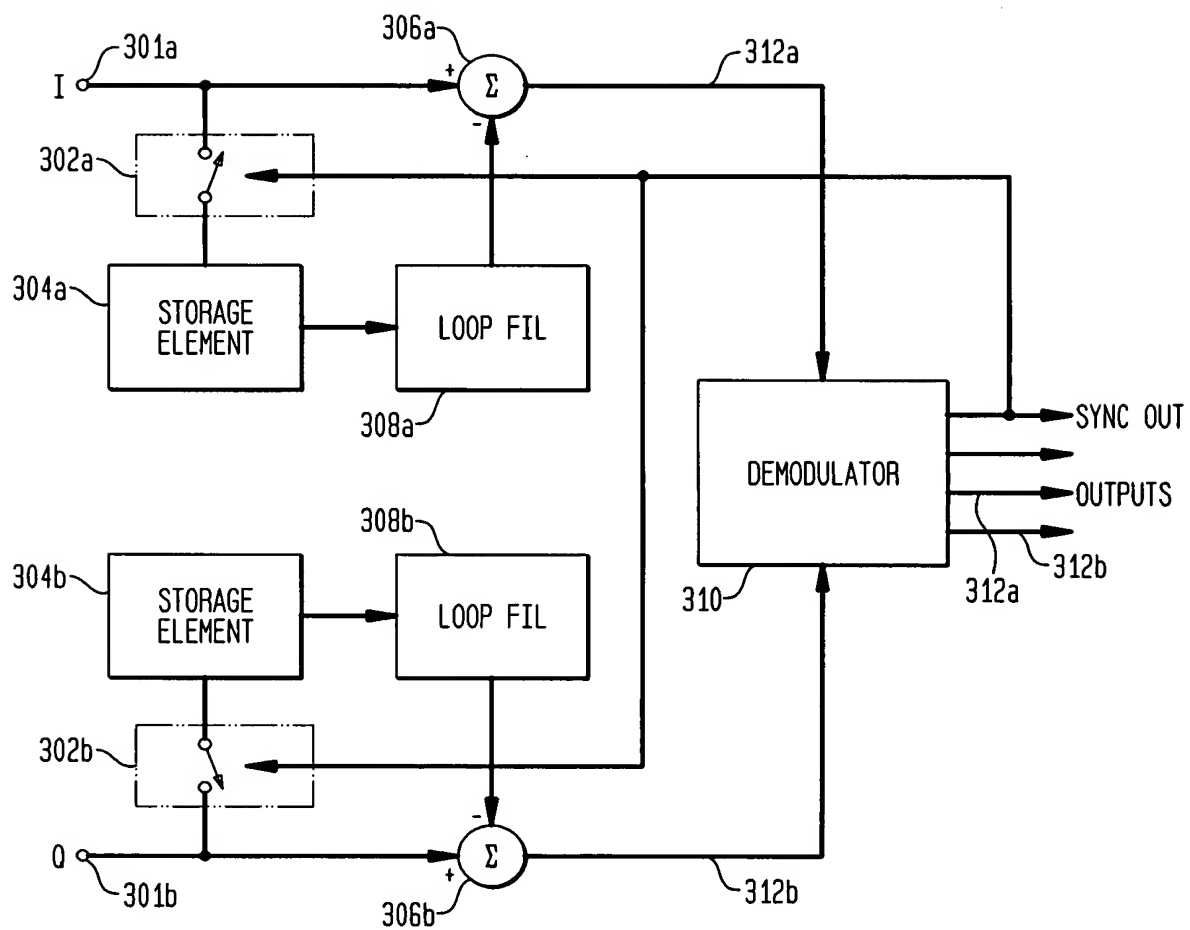


FIG. 3



**FIG. 4**

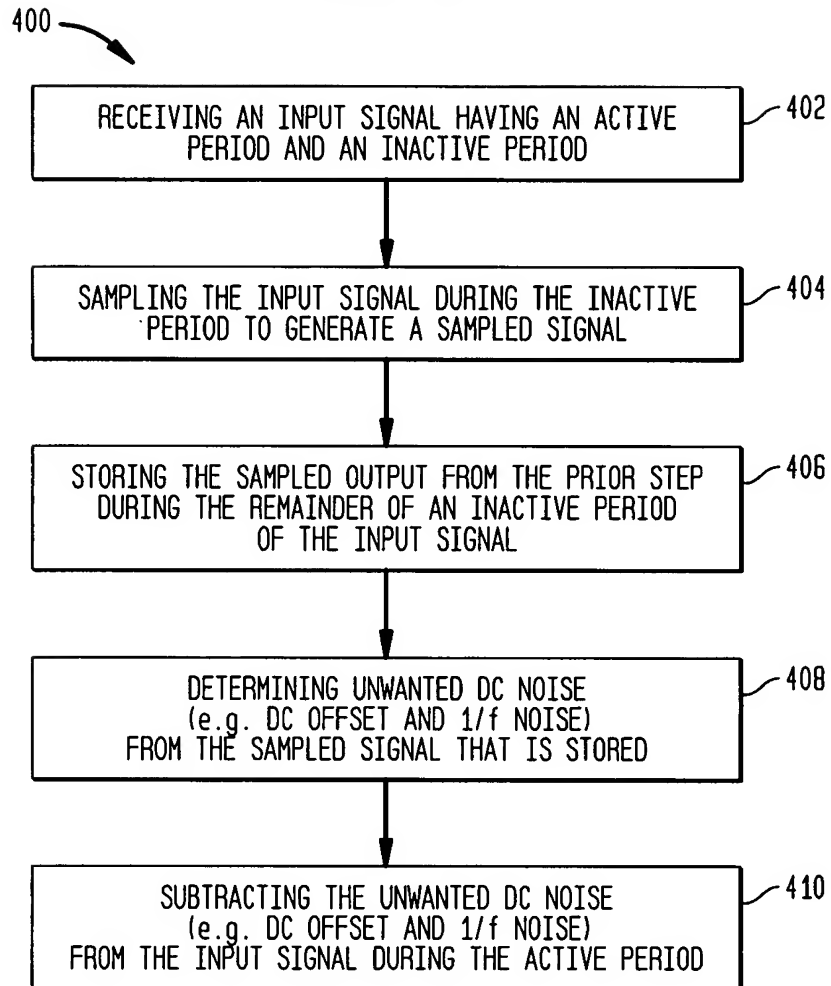
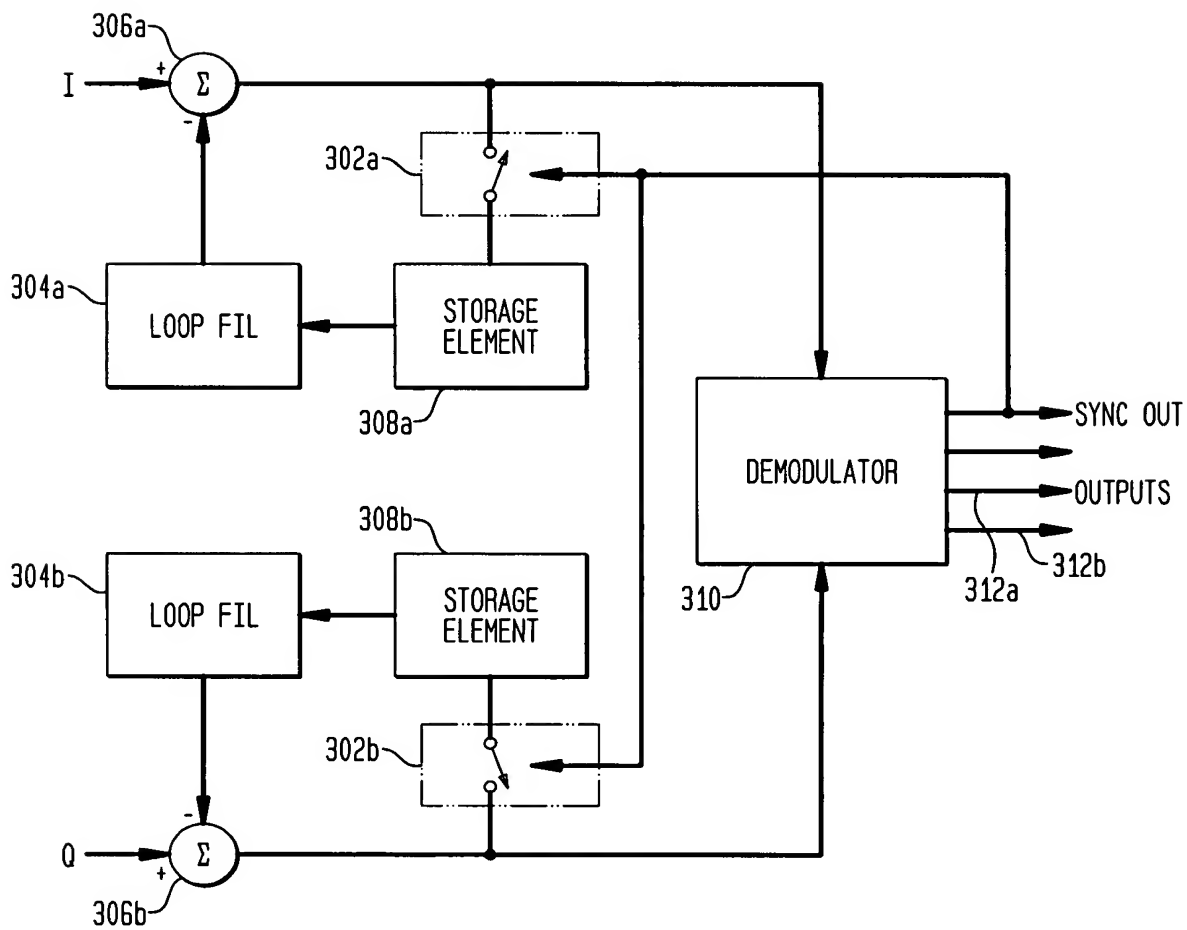


FIG. 5



**FIG. 6**

